

### **Amendment to the Specification**

**[0071]** The transient time extending part 243 has one or more time extending elements 245a and ~~245b~~ 245d to prolong a transient time by a predetermined time to output a second nozzle selection signal according to the first nozzle selection signal. Here, the transient time is the time period during which the potential level of the signal (first nozzle selection signal) inputted from the level converter 241 to the switching unit 250 is converted from a first signal level to a second signal level and vice versa. Also, the first signal level is a signal of logic "high" and the second signal level is a signal of logic "low."

**[0075]** The second inverter 245 includes a first PMOS 245a having a source connected to a voltage supply Vdd and a gate and a drain commonly connected to each other, a second PMOS 245b having a source connected to the drain of the first PMOS 245a and a gate connected to an output terminal of the first inverter 244, a first NMOS 245c having a gate ~~and a source~~ commonly connected to the gate of the second PMOS 245b and a drain connected to the drain of the second PMOS 245b to form an output terminal of the second inverter 245, and a second NMOS 245d having a drain and a gate commonly connected to the source of the first NMOS 245c and a source connected to the ground.